

CURRICULUM VITAE

Ms. SUNANDA C V *M.Tech*

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Academic Qualifications

- M.Tech, Master of technology in Electronics, VTU, Belgaum. 2012.
- B.E, Bachelor of Engineering in Electrical and Electronics Engineering, VTU, Belgaum. 2008.

Experience

- 5 years of experience in Teaching.

Academic:

- Assistant Professor, Department of Electrical and Electronics Engineering, RR Institute of Technology, Bangalore, 25th July 2013.
- Lecture/Lecturer, Dept. of Electrical and Electronics Engineering, S.C.T. Institute of Technology, Bangalore 03rd April 2009 to 20th Nov 2010.

Evaluation and others:

- Answer script valuation for Theory subjects at VTU centers.
- Test Coordinator for EEE department.
- Internal and External examiner for practical examination.
- Invigilation for VTU Theory examination.

Memberships of Professional Bodies

- Indian Society of Technical Education.

Study Materials developed

- Laboratory Manual for Transformers and induction machines.
- Laboratory Manual for Control systems.

Course Subjects taught

UG Level

- Basic Electrical Engineering
- Electrical and Electronic Measurements and Instrumentation
- Control Systems
- Field Theory
- Transformers and Induction Machines
- Signals and Systems
- Transmission and Distribution
- Switch Gear and Protection
- Digital Signal Processing
- Electrical Power Utilization
- Power System Planning
- Renewable Energy Source
- Logic Design Lab
- Power Electronics Lab
- Measurements and Circuit Simulation Lab
- Transformers and Induction Machines Lab
- Control Systems Lab

Student Projects Guided

- UG: 3 Batches (4 Students in each batche).

Curricular and co-curricular activities

Department Library Coordinator, Anti-Ragging Committee, Hostel and canteen committee, Class teacher, Mentoring of Students Committee, Counseling Coordinator Committee .

Programs/Seminars/Conferences Attended

1. Participated in staff and student development programmer on “Advances in Electronic Warfare System and Software Defined Radio”held on 19thMarch,2011, organized by the Dept. of EC, Sir M.VIT, Bangalore.
2. Participated in staff and student development programmer on “FPGA for DSP and Communications” organized by the Dept. of EC and Telecommunication Engineering, Sir M.VIT, Bangalore.
3. Attended two days Workshop on “Emerging Research Areas in Electrical Engineering” held on 27th and 28th Sep 2013.