

CURRICULUM VITAE



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Mrs. Parimala Gandhi G

Academic Qualifications

- Pursuing Ph.D(Aerospace control) in Electronics and Communication Engineering ,Oxford college of Engineering Bangalore,VTU.
- M.Tech in Embedded System Design from Dr. MGR University, Chennai, 2007
- B.E, Bachelor of Engineering in Electronics and Instrumentation , Government College of Technology,Coimbatore, Bharathiyar University Coimbatore 2000.

Experience

- 13.5years of experience in Teaching and Research

Academic:

- Associate Professor, Department of Electronics and Communication Engineering RR Institute of Technology, Bangalore, 29th July 2013
- Assistant Professor, Department of Electronics and Communication Engineering, Oxford College of Engineering Bangalore from March 2011 to July-2013
- Sr.Lecturer, Dept. Electronics and Communication Engineering, Oxford College of Engineering Bangalore from Jan 2008 to February 2011
- Lecturer, Dept. Electronics and Communication Engineering, Oxford College of Engineering Bangalore from 29th September 2002 to Dec 2007
- Lecturer, Dept. Electronics and Instrumentation Engineering, Siddaganga Institute of Technology , Tumkuru from 10th October 2001 to July-2002

Additional Courses:

- HDSE certificate for programming language (FOXPRO,C++,JAVA,Oracle With Developer from APTECH 1997-1999
- E-Commerce (Web Designing Course) From NIIT, 2001
- Certificate course in Sanskrit 1990-1992
- Certificate Course in Hindi 1989-92

Awards:

- The Headmistresses Academic Council ,Salem Circle Award for securing cent percent in 10th standard Mathematics
- Received award for securing first place among five districts in VeerashaivaLingayat Educational Trust , Coimbatore
- Received "Tamil Excellence" award , Tamil Teacher sangam
- Cent Percent Resultaward for Digital Image Processing, Advanced Computer Architecture, Advanced Digital Design using VHDL, Biomedical Signal Processing, Programming in C++,Image Processing, ASIC Design, Modern DSP
- Received 1st place in fireless cooking event , KalataRRanga 2015 at RRIT-Bangalore,7th March 2015

Evaluation and others:

- Deputy Chief Superintendent of VTU Examination.
- In a team worked for accreditation (NBACoordinator)for the department of Electronics and CommunicationEngg. at TOCE and got success
- Active Coordinator in preparing MODROB Project and got success.
- LIC Coordinator, Department of Electronics and CommunicationEngg. at TOCEfrom 2007-13

- Worked in several confidential assignments of examination of VTU, Belgaum

Memberships of Professional Bodies

- IEEE member
- Indian Society of Technical Education.

Expert Lectures delivered

- Resource Person for SDP-10(AICTE Sponsored) on Adders, Department of ECE, The Oxford College of Engg, Bangalore 15th June 2010
- Signal processing, C programming concepts, pointers The Oxford College of Engg, Bangalore

Study Materials developed

- Laboratory Manuals for AEC Lab, DSP Lab, VHDL lab, VLSI Lab and Modern DSP Lab, CCN and Microcontroller.

Books Authored /Edited

1. Proceedings of National Conference on “CCVS-09” Department of Electronics and Communication Engineering at TOCE Bangalore, 21-22 April 2009
2. College Newsletter “Sandesh” at TOCE, Bangalore”- Dec-2011 Vol.no 08
3. Proceedings of National Conference on “CCVS-10”, Department of Electronics and Communication Engineering at TOCE Bangalore, 21-22 April 2010

Course Subjects taught

UG Level

- Programming in C++,
- Signals and System
- Digital Signal Processing
- Digital Switching System
- Fundamentals of CMOS VLSI
- Fundamental of HDL
- Analog Electronics Circuits
- DSP Algorithm and Architecture
- Image Processing
- Biomedical Signal Processing
- Telecommunication and Switching Network

PG Level

- ASIC Design
- Advance Digital System design using VHDL
- Advanced Computer Architecture
- Modern DSP
- CMOS VLSI Design
- Advances in VLSI Design

Student Projects Guided

- UG: 15
- PG: 05

Programs/Seminars/Conferences Organized

1. Organizing Chair, of National Conference on Advancement in Science, Engineering, & Technology-ASET2016, RRIT Bangalore, 6-7 May 2016
2. Project Exhibition “Meraki-2016”, RRIT, Bangalore- 11th May 2016
3. Industrial Visit to “KGTIT”, Department of Electronics and Communication Engineering, RRIT, 24th March 2016

4. Oneday Work shop on “Image processing using MATLAB” at Department of Electronics and Communication Engineering, RRIT , 16th March 2016
5. Organising Coordinator for one week VTU-VGST FDP on “Video and Image processing using LABVIEW/DSP” at department of ECE ,TOCE, 13th -17th March 2012.
6. Coordinator for National symposium “RMTECE-09”(DRDO,VTU Sponsored), at Department of ECE ,TOCE,Bangalore 28-29 Oct09
7. Organizing Coordinator for one day workshop on “UWCST-11”,IEEE Student Chapter at Department of ECE & CSE, TOCE,Bangalore 1-2nd Dec-11
8. Coordinator for two day Conference CCSV-09, CCSV-10, (DRDO,VTU Sponsored)at Department of Electronics and Communication Engineering TheOxford College of Engineering, 21-22nd April Bangalore
9. Coordinator for two Week SDP VLSI for Multimedia Applications & Telecommunication Systems (AICTE Sponsored),at Department of Electronics and Communication, 14th June to 26th June 2010 TOCE Bangalore

Programs/Seminars/workshops Attended

- Two-day workshop on “Intellectual Property Rights” organized by e-learning Centre,Mysuru at VTU regional office,10-11 July 2015
- Three day workshop on “Research methodologies and Latex”organized by e-learning centre , Mysuru at VTU regional office, 18th to 20th March 2015
- National Symposium on “ Reliability and Fault Tolerance” , 26th-27th July 2013 at REVA IT.
- Four-day workshop on “Advanced Microcontroller MSP-430” at MSRIT , 4th -7thFeb-2013
- Two-day workshop on “Octave and Latex” organized by RNSIT on 23rd -24th Jan-2013
- One day Workshop on “Optical Sensor , Systems and applications” Sponsored by IEEE Photonic Society ,IISC Bangalore at TOCE- Bangalore, 3rd Nov-2012.
- One day workshop on "SCILabs", organized by IEEE Bangalore section at UTL Technology on 20th Oct-2012
- IEEE Workshop on “Advanced Linux Concepts”, Organized by IEEE Bangalore Branch in The Oxford College of Engineering, Bangalore on 23rd and 24th of Sep, 2011.
- Two day national level workshop on “Video and Image Processing UsigLabview/DSP”, organized by PESSE, Bangalore,14-15th July 2011.
- One week AICTE-ISTE sponsored STTP on “Fundamentals of Cryptography and Applications” organized by Nagarjuna College of Engineering and Technology , Bangalore on July 7-11th 2008
- One day FDP on "Building Embedded System Using RTOS", organized by UTL Technology, 20th Feb-2008.
- One day workshop on "Advanced Communication Lab",organized by CMRIT,Bangalore 16th Jan 2009
- Participated two day workshop on “ Research Methodologies” organized by Velammal Engineering College, Chennai , 23-24th Feb 2009
- Faculty Development Programme(TEQIP) organized by “BMS” College of Engineering , Bangalore,16-21 July 2007.
- Participated /Coordinated various workshops organized by the Department of Electronics and Communication Engineering, The Oxford College of Engineering, Bangalore.

Publications/Conference

1. **G Parimala Gandhi**, NagarajRamRao, ManojSinghJohns, et al, “Robust Flare Phase Auto Landing of Aircrafts, based on Modified ASBO, Accepted
2. G Parimala Gandhi , NagarajRamRao, Naveen N Padi“Bibliographical Review on Fuzzy Inteference Model” Communicated
3. G Parimala Gandhi , NagarajRamRao, SapamHema Devi “Designing Fuzzy Logic

- Controller for Auto Landing of Flight” International Journal of Scientific & Engineering Research, Volume 7, Issue 5, May-2016 231 ISSN 2229-5518
4. Apoorva, G Parimala Gandhi “Hypervisor based Fault tolerance using Virtual Lockstep” on March 30-31, at International Academic and Industrial Research Solution(ICITEC-13) , BangaloreIAIRS ISBN 978-93-82359-35-7pg no; 86-89
 5. Naveen N Padi,Nagaraj,Prakash, Abhijit , G Parimala Gandhi,“ Aerodynamic Model Analysis and 6-DOF Flight Simulation model and Design Using Matlab – Simulink” in proceedings of Proceedings of National Conference on Advancement in Science, Engineering, & Technology-ASET2016, RRIT Bangalore, 6-7 May 2016
 6. G Parimala Gandhi , SapamHema Devi “Designing Fuzzy Logic Controller for Auto Landing of Flight” Proceedings of National Conference on Advancement in Science, Engineering, & Technology-ASET2016, RRIT Bangalore, 6-7 May 2016
 7. Naveen N Padi, G Parimala Gandhi, SapamHema Devi “Bibliographical Review on Fuzzy Inteference Model”Proceedings of National Conference on Advancement in Science, Engineering, & Technology-ASET2016, RRIT Bangalore, 6-7 May 2016
 8. Deepa H , G Parimala Gandhi “Tree Species Discrimination in Tropical Forest using RBF-ANN” in proceedings of NCRSEM-15 at PESIT, Shivamoga on May-02-15
 9. Anitha , Sreelakshmi ,G Parimala Gandhi “Implementation of Boiler control and Real Time Process control and Development of GUI for its control and Monitoring” ,in proceedings of NCCVS-14 at KSSEM on 15 May-14
 10. BasavarajDodda G Parimala Gandhi “ Intelligent Image Content Authentication with Neural Approach “ at the international Conference on Recent Advances in Computer Engineering and Technology-2013at IRD India Cochin on 9th June 2013(ICRACET_9 Junecohin13_IRD_0081)
 11. G Parimala Gandhi, Priyamvada "Analog Phase modulator using Varactor Diode" at National Conference on ETECE-12,28th May-12 at Canara Engineering College, Mangalore
 12. G.Parimala Gandhi , "Production Monitoring System" at National conference on PORGEM-12 3rd June-12 ,Acharaya Institute of Technology, Bangalore